

Model: Crucial_CT480M50

S/N: 1346095C0B42

Disk Erasure Report

Page 1 - Erasure Status



Organisation Performing The Disk Erasure

Business Name: **GridJet**

Business Address:

Contact Name: **GDW-LDS-6**

Contact Phone:

Customer Details

Name: **Gridjet**

Address:

Contact Name:

Contact Phone:

Disk Information

Make/Model: **Crucial_CT480M50**

Serial: **1346095C0B42**

Size(Apparent): **480 GB, 480103981056 bytes**

Bus: **ATA-SSD**

Size(Real): **480 GB, 480103981056 bytes**

Disk Erasure Details

Start time: **2025/02/17 04:47:00**

End time: **2025/02/17 12:47:48**

Duration: **08:00:48**

Status: **ERASED**

Method: **PRNG Stream**

PRNG algorithm: **XORshiro256**

Final Pass(Zeros/Ones/None): **Zeros**

Verify Pass(Last/All/None): **Verify Last**

*Bytes Erased: **480103981056, (100.00%)**

Rounds(completed/requested): **1/1**

HPA/DCO: **No hidden sectors**

HPA/DCO Size: **No hidden sectors**

Errors(pass/sync/verify): **0/0/0**

Throughput: **49 MB/sec**

Information:

* bytes erased: The amount of drive that's been erased at least once

Technician/Operator ID

Signature:

Name/ID: **Auto Wipe**



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smartctl 7.2 2020-12-30 r5155 [x86_64-linux-5.14.0-503.23.1.el9_5.x86_64] (local build)
copyright (c) 2002-20, bruce allen, christian franke, www.smartmontools.org

=== start of information section ===

model family: Crucial/Micron Client SSDs
device model: Crucial_CT480M500SSD1
serial number: 1346095C0B42
lu wwn device id: 5 00a075 1095c0b42
firmware version: MU03
user capacity: 480,103,981,056 bytes [480 GB]
sector sizes: 512 bytes logical, 4096 bytes physical
rotation rate: Solid State Device
form factor: 2.5 inches
trim command: Available, deterministic, zeroed
device is: In smartctl database [for details use: -P show]
ata version is: ACS-2, ATA8-ACS T13/1699-D revision 6
sata version is: SATA 3.1, 6.0 Gb/s (current: 6.0 Gb/s)
local time is: Mon Feb 17 12:48:18 2025 GMT
smart support is: Available - device has SMART capability.
smart support is: Enabled

=== start of read smart data section ===

smart overall-health self-assessment test result: PASSED

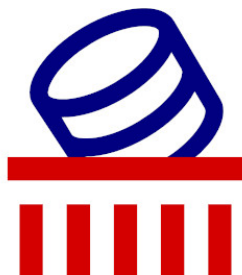
general smart values:

offline data collection status: (0x80)Offline data collection activity
was never started.
auto offline data collection: Enabled.
self-test execution status: (0)The previous self-test routine completed
without error or no self-test has ever
been run.
total time to complete offline
data collection: (2235) seconds.
offline data collection
capabilities: (0x7b) SMART execute Offline immediate.
auto offline data collection on/off support.
suspend offline collection upon new
command.
offline surface scan supported.
self-test supported.
conveyance self-test supported.
selective self-test supported.
smart capabilities: (0x0003)Saves SMART data before entering
power-saving mode.
supports smart auto save timer.
error logging capability: (0x01)Error logging supported.
general purpose logging supported.
short self-test routine
recommended polling time: (2) minutes.
extended self-test routine
recommended polling time: (37) minutes.
conveyance self-test routine
recommended polling time: (3) minutes.
sct capabilities: (0x0035)SCT Status supported.
sct feature control supported.
sct data table supported.

smart attributes data structure revision number: 16

vendor specific smart attributes with thresholds:

id#	attribute_name	flag	value	worst	thresh	type	updated	when_failed	raw_value
1	raw_read_error_rate	0x002f	100	100	000	pre-fail	always	-	265
5	reallocate_nand_blk_cnt	0x0033	099	099	000	pre-fail	always	-	122
9	power_on_hours	0x0032	100	100	000	old_age	always	-	62623
12	power_cycle_count	0x0032	100	100	000	old_age	always	-	19



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171	program_fail_count	0x0032	100	100	000	old_age	always	-	0
172	erase_fail_count	0x0032	100	100	000	old_age	always	-	0
173	ave_block-erase_count	0x0032	024	024	000	old_age	always	-	2297
174	unexpected_power_loss_ct	0x0032	100	100	000	old_age	always	-	16
180	unused_reserve_nand_blk	0x0033	000	000	000	pre-fail	always	-	8097
183	sata_interfac_downshift	0x0032	100	100	000	old_age	always	-	0
184	error_correction_count	0x0032	100	100	000	old_age	always	-	0
187	reported_uncorrect	0x0032	100	100	000	old_age	always	-	5
194	temperature_celsius	0x0022	067	063	000	old_age	always	-	33 (min/max -10/37)
196	reallocated_event_count	0x0032	100	100	000	old_age	always	-	138
197	current_pending_ecc_cnt	0x0032	100	100	000	old_age	always	-	0
198	offline_uncorrectable	0x0030	100	100	000	old_age	offline	-	0
199	udma_crc_error_count	0x0032	100	100	000	old_age	always	-	0
202	percent_lifetime_remain	0x0031	024	024	000	pre-fail	offline	-	76
206	write_error_rate	0x000e	100	100	000	old_age	always	-	0
210	success_rain_recov_cnt	0x0032	100	100	000	old_age	always	-	0
246	total_lbas_written	0x0032	100	100	---	old_age	always	-	362593602549
247	host_program_page_count	0x0032	100	100	---	old_age	always	-	2741534034
248	ftl_program_page_count	0x0032	100	100	---	old_age	always	-	579075926

smart error log version: 1

ata error count: 0

cr = command register [hex]

fr = features register [hex]

sc = sector count register [hex]

sn = sector number register [hex]

cl = cylinder low register [hex]

ch = cylinder high register [hex]

dh = device/head register [hex]

dc = device command register [hex]

er = error register [hex]

st = status register [hex]

powered_up_time is measured from power on, and printed as

ddd+hh:mm:ss.sss where DD=days, hh=hours, mm=minutes,

ss=sec, and sss=millisec. it "wraps" after 49.710 days.

error 0 occurred at disk power-on lifetime: 0 hours (0 days + 0 hours)

when the command that caused the error occurred, the device was in an unknown state.

after command completion occurred, registers were:

er st sc sn cl ch dh

-- -- -- -- --

40 51 80 80 bb 32 40 error: UNC at LBA = 0x0032bb80 = 3324800

commands leading to the command that caused the error were:

cr fr sc sn cl ch dh dc powered_up_time command/feature_name

-- -- -- -- --

60 00 80 80 bb 32 40 00 02:34:18.976 READ FPDMA QUEUED

60 00 78 08 bb 32 40 00 02:34:18.976 READ FPDMA QUEUED

61 01 08 c8 9a 86 40 00 02:34:18.976 WRITE FPDMA QUEUED

61 00 18 b0 9a 86 40 00 02:34:18.976 WRITE FPDMA QUEUED

61 da 08 00 d4 58 40 00 02:34:18.976 WRITE FPDMA QUEUED

error -1 occurred at disk power-on lifetime: 0 hours (0 days + 0 hours)

when the command that caused the error occurred, the device was in an unknown state.

after command completion occurred, registers were:

er st sc sn cl ch dh

-- -- -- -- --

40 51 08 98 9f 31 40 error: WP at LBA = 0x00319f98 = 3252120

commands leading to the command that caused the error were:

cr fr sc sn cl ch dh dc powered_up_time command/feature_name

-- -- -- -- --



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```
61 da 08 00 d4 58 40 00 46d+20:21:37.344 WRITE FPDMA QUEUED
61 00 08 30 04 dd 40 00 46d+20:21:37.344 WRITE FPDMA QUEUED
61 00 08 f8 1b bd 40 00 46d+20:21:37.344 WRITE FPDMA QUEUED
61 da 08 10 04 9d 40 00 46d+20:21:37.344 WRITE FPDMA QUEUED
61 00 08 a8 0b 6d 40 00 46d+20:21:37.344 WRITE FPDMA QUEUED
```

error -2 occurred at disk power-on lifetime: 0 hours (0 days + 0 hours)
when the command that caused the error occurred, the device was in an unknown state.

after command completion occurred, registers were:

er st sc sn cl ch dh

-- -- -- -- --

40 51 08 c8 57 8a 40 error: UNC at LBA = 0x008a57c8 = 9066440

commands leading to the command that caused the error were:

cr	fr	sc	sn	cl	ch	dh	dc	powered_up_time	command/feature_name
----	----	----	----	----	----	----	----	-----------------	----------------------

-- -- -- -- --

60	00	08	a0	ed	22	40	00	9d+04:46:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	da	40	80	ba	89	40	00	9d+04:46:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	00	10	f0	7f	73	40	00	9d+04:46:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	da	50	00	ac	0f	40	00	9d+04:46:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	da	08	f8	92	11	40	00	9d+04:46:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

error -3 occurred at disk power-on lifetime: 0 hours (0 days + 0 hours)
when the command that caused the error occurred, the device was in an unknown state.

after command completion occurred, registers were:

er st sc sn cl ch dh

-- -- -- -- --

40 51 40 80 51 4a 40 error: UNC at LBA = 0x004a5180 = 4870528

commands leading to the command that caused the error were:

cr	fr	sc	sn	cl	ch	dh	dc	powered_up_time	command/feature_name
----	----	----	----	----	----	----	----	-----------------	----------------------

-- -- -- -- --

60	da	40	80	26	4b	40	00	8d+09:30:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	da	38	80	51	4a	40	00	8d+09:30:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	00	38	80	5a	49	40	00	8d+09:30:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	00	40	c0	aa	4a	40	00	8d+09:30:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

60	da	08	80	04	b5	40	00	8d+09:30:52.192	READ FPDMA QUEUED
----	----	----	----	----	----	----	----	-----------------	-------------------

error -4 occurred at disk power-on lifetime: 0 hours (0 days + 0 hours)
when the command that caused the error occurred, the device was in an unknown state.

after command completion occurred, registers were:

er st sc sn cl ch dh

-- -- -- -- --

40 51 08 80 eb 2f 40 error: WP at LBA = 0x002feb80 = 3140480

commands leading to the command that caused the error were:

cr	fr	sc	sn	cl	ch	dh	dc	powered_up_time	command/feature_name
----	----	----	----	----	----	----	----	-----------------	----------------------

-- -- -- -- --

61	00	08	e0	82	30	40	00	39d+09:17:01.376	WRITE FPDMA QUEUED
----	----	----	----	----	----	----	----	------------------	--------------------

61	da	08	30	82	30	40	00	39d+09:17:01.376	WRITE FPDMA QUEUED
----	----	----	----	----	----	----	----	------------------	--------------------

61	da	08	68	81	30	40	00	39d+09:17:01.376	WRITE FPDMA QUEUED
----	----	----	----	----	----	----	----	------------------	--------------------

61	00	08	28	81	30	40	00	39d+09:17:01.376	WRITE FPDMA QUEUED
----	----	----	----	----	----	----	----	------------------	--------------------

61	00	08	b8	80	30	40	00	39d+09:17:01.376	WRITE FPDMA QUEUED
----	----	----	----	----	----	----	----	------------------	--------------------

smart self-test log structure revision number 1

num	test_description	status	remaining	lifetime(hours)	lba_of_first_error
# 1	vendor (0xff)	completed without error	00%	62608	-
# 2	vendor (0xff)	completed without error	00%	62573	-
# 3	vendor (0xff)	completed without error	00%	62333	-
# 4	vendor (0xff)	completed without error	00%	62319	-
# 5	vendor (0xff)	completed without error	00%	62206	-



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# 6	vendor (0xff)	completed without error	00%	62165	-
# 7	vendor (0xff)	completed without error	00%	62109	-
# 8	vendor (0xff)	completed without error	00%	62018	-
# 9	vendor (0xff)	completed without error	00%	61954	-
#10	vendor (0xff)	completed without error	00%	61863	-
#11	vendor (0xff)	completed without error	00%	61759	-
#12	vendor (0xff)	completed without error	00%	61614	-
#13	vendor (0xff)	completed without error	00%	61514	-
#14	vendor (0xff)	completed without error	00%	61421	-
#15	vendor (0xff)	completed without error	00%	61304	-
#16	vendor (0xff)	completed without error	00%	61218	-
#17	vendor (0xff)	completed without error	00%	61110	-
#18	vendor (0xff)	completed without error	00%	60996	-
#19	vendor (0xff)	completed without error	00%	60848	-
#20	vendor (0xff)	completed without error	00%	60752	-
#21	vendor (0xff)	completed without error	00%	60658	-

smart selective self-test log data structure revision number 1

span	min_lba	max_lba	current_test_status
1	0	0	not_testing
2	0	0	not_testing
3	0	0	not_testing
4	0	0	not_testing
5	0	0	not_testing

selective self-test flags (0x0):

after scanning selected spans, do not read-scan remainder of disk.
if selective self-test is pending on power-up, resume after 0 minute delay.